

## What is the Myricom Product Code hardware naming scheme for Myri-10G adapters?

### Model:

ARC Series E Adapters

### Software:

DBL, SNF, MVA

### Operating System:

N/A

### Information:

The product code naming scheme for the Myri-10G network adapters is comprised of 5 fields:

10G

{PCIE, PCIE2, PCIE3}

8{A,B,C,D}

{S,R,C,I,T,BE}

{DBL3,SNF3,MVA1}

**10G** denotes that it is a 10-Gigabit Ethernet network adapter.

**PCIE** indicates that the adapter is “Gen1” (2.5 GT/s) PCI-Express adapter. **PCIE2** indicates “Gen2” (5.0 GT/s) PCI Express, and **PCIE3** indicates “Gen3” (5.0 GT/s) PCI Express.

**8** indicates that the adapter is an x8 (8 lane) adapter. The **A** indicates that it is a first generation Myri-10G adapter, the **B** indicates a second generation adapter, **C** indicates a third generation adapter, and **D** indicates a fourth generation adapter.

The fourth field indicates the Myri-10G **PHY** for the network port.

**S** denotes SFP+

**R** denotes XFP

**C** denotes 10Gbase-CX4

**I** denotes BladeCenterH HSEC

**T** denotes 10Gbase-T

**BE** denotes PC/104

The **+DBL3**, **+SNF3**, **+MVA1** suffix added to the hardware product code (example: 10G-PCIE2-8C2-2S+DBL) denotes that the network adapter was purchased with a software license. If there is no +X denoted when purchased, then the adapter was purchased for use with a standard 10GbE driver, Myri10GE.

No **+X** denotes that the adapter was purchased for use with our freely-available standard 10GbE driver, Myri10GE for Content Creation.

**+DBL3**: denotes that the adapter was purchased with a DBL v3 license.

**SNF3**: denotes that the adapter was purchased with a Sniffer10Gv3 license.

**+MVA1**: denotes that the adapter was purchased with an MVA license.

**The Myri-10G “Gen1” adapters** are PCI-Express x8 (8 lane) adapters running at 10 +10 Gb/s data rate, full duplex, with 2MB of SRAM. Each Myri-10G “8A” adapter contains a first-generation LanaiZ8E chip operating at 313MHz. Each Myri-10G “8B” adapter contains a second-generation LanaiZ8E chip operating at 364.6 MHz Their naming scheme is expressed as follows:

10G-PCIE-8A-y  
10G-PCIE-8B-y

Where PCIE denotes that these are “Gen1” PCI Express, the -8 denotes that these are PCI-Express x8 (8 lane) adapters, the “A” denotes that this adapter contains a LanaiZ8E chip, the “B” denotes that this adapter contains a LanaiZ8ES chip and:

**Y** indicates the **Myri-10G PHY**  
**S** denotes **SFP+**  
**R** denotes **XFP**  
**C** denotes **10GBase-CX4**  
**I** denotes **BladeCenterH HSEC**

**The Myri-10G “Gen2” adapters** are PCI-Express x8 (8 lane) adapters running at 20 +20 Gb/s data rate, full duplex, with 2MB of SRAM. Each Myri-10G “8B2” or “8C2” adapter contains two LanaiZ8ES chips operating at 364.6 MHz Their naming scheme is expressed as follows:

10G-PCIE2-8B2-2y  
10G-PCIE2-8C2-2y

Where PCIE2 denotes that these are “gen2” PCI Express, the -8 denotes that these are PCI-Express x8 adapters, the “B2” or “C2” denotes that this adapter contains two LanaiZ8Es chips, and

**Y** indicates the **Myri-10G PHY**  
**T** denotes **10GBase-T**

In addition to the two-port x8 Gen2 10G-PCIE2-8C2-2T adapter, there is also a single-port x8 Gen2 10G-PCIE2-8C-T adapter.

**S** denotes **SFP+**  
**C** denotes **10GBase-CX4**

<u>Revision</u>	<u>Date</u>	<u>Change</u>
1	6/30/2016	Initial Draft
2	8/18/2016	Feedback Edits